



G. H. Raisoni Institute of Engineering & Technology

(An Autonomous Institute Affiliated to Rashtrasant Tukadoji Maharaj Nagpur University, Nagpur)

Accredited by NAAC with 'A+' Grade

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Department of Electrical Engineering

Organizes
Circuit making competition using breadboard
under
“IEI students chapter”
Session 2019-20

The Department of Electrical Engineering, G.H. Raisoni Institute of Engineering & Technology, Nagpur had organized 'Circuit Making Competition using Breadboard' for the students of Shraddha Park Campus on 16 September 2019. Total 54 groups of participants including 215 students from the various departments of GHRIET, GHRAET and GHRP had participated in this competition. Each group of participants was given the common task of making the connections for the circuit "Bias Circuit with an Emitter Feedback Resistor" during the competition. All the necessary components including the transistors, resistors and capacitors, connecting wires along with the breadboard and circuit diagram were provided separately to the individual groups.

Prof. M.J. Katira (HoD, EE dept. GHRIET), Prof. Pratik R. Hajare (HoD, ETC, GHRIET), Prof. Vipin Bhure (HoD, ETC, GHRAET), Prof. Ravi Mishra, Prof. Rajshree Thakre and Prof. Niraj Dewangan were the judges for the competition. They examined the circuit made by each group on the basis of appropriateness of the connections & circuit compactness. The groups led by Sampriy Nitnaware from EE 5th semester (GHRIET), Aditya Gotmare from EC 5th semester (GHRP) and Sagar gupta from EE 5th semester (GHRIET) had secured 1st, 2nd and 3rd positions respectively in the competition. The students enthusiastically participated in the competition. Dr. Prashant Y. Pawade (Director, GHRIET) and Prof. M.J. Katira (HoD, EE dept. GHRIET) addressed the students and appreciated them for showing their interest towards learning the circuit design by participating in the event in large numbers. They congratulated the winners of the competition and also encouraged the students to participate in such events in future also as that would help them to enhance their knowledge and skill on circuit designing for industrial applications. The event was coordinated by Prof. Sunil Kumar Shukla (Asst. Prof., EE Deptt., GHRIET) and Prof. D.U. Sarkar (Asst. Prof., EE Deptt., GHRIET).

